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Commissioner for Patents
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RE: KAJIHARA, et al. - US Appl'n No. 09/989,242
Att'y Docket No. 501.32049RV2 / 329201392US5
Examiner J. Clark, AU 2815, USPTO Conf. No. 9603

SUBMISSION OF AMENDMENT

Sir:

Applicant hereby submits the attached AMENDMENT (45 pages) for entry in
the above-noted application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached **AMENDMENT (45 pages)** is being
FORMALLY TRANSMITTED via the USPTO Main Facsimile No. 571-273-8300 on
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501.32049RV2/329201392US5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/989,242

Filed: 21 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 9603

AMENDMENT

Mail Stop Amendment

Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

27 August 2004

Sir:

In response to the 20 March 2006 Office Action, the following amendments
and remarks are respectfully submitted.